Attorney's Docket No.: 10559-615001/P12854 Intel Corporation



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Matthew J. Adiletta et al. Art Unit: 2111 Serial No.: 10/053,172 Examiner: K. Kim

Filed : January 17, 2002
Assignee : Intel Corporation
Title : FUNCTIONAL PIPELINES

Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information

Disclosure Statement and documents listed on form PTO-1449.

Also, enclosed are copies of the International Search Reports issued in related foreign counterpart applications.

Kindly accept this Information Disclosure Statement under Rule 97(c)(2). The rule 17(p) certification fee of \$180 is enclosed.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 24, 2004

Date of Deposit

Signature

Jennifer H. Payne

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11/30/2004 SSITHIB1 00000040 10053172 01 FC:1806 180.00 QP Applicant : Matthew J. Adiletta et al. Attorney's Docket No.:10559-615001

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Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: November 24, 2004

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Sheet	1	of	1

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-615001	Application No. 10/053,172	
		Applicant Matthew J. Adiletta et al.		
(Use several sh	eets if necessary)	Filing Date	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
• • • • • • • • • • • • • • • • • • • •	AA	09/473,571	N/A	Wolrich, et al.			12/28/1999
	AB	09/475,614	N/A	Wolrich, et al.			12/30/1999
	AC	5,913,059	06/1999	Torii			
<u> </u>	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner	Desig.	Document	Publication	Country or			Trans	lation
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL	WO 01/16782	03/2001	WIPO				
	AM							
	AN							
	AO					:		
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)				
Examiner	Desig.			
Initial	ID	Document		
	AQ	Moors, et al., "Cascading Content-Addressable Memories", IEEE Micro, 12(3):56-66 (1992).		
	AR	Shah, Niraj, "Understanding Network Processors", Masters Thesis Submitted to the University of California, Berkeley, September 4, 2001.		
	AS	International Search Report from PCT/US03/01578; mailed February 19, 2004.		
	AT	International Search Report from PCT/US03/01580; mailed April 21, 2004.		

Examiner Signature	Date Considered				
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with					
next communication to applicant.					